



FFW

In re the application of:
Carl Larry Ostrowski, et al.
Serial No.: 10/759,917
Filed: January 16, 2004

Docket No.: 550,701
Group Art Unit: 2829
Examiner: Minh N. Tang
Date: April 5, 2005

FOR: SINGLE-TRANSISTOR TWO-RESISTOR CIRCUIT WHICH
TRANSLATES TEST SIGNALS TO SELECTABLE VOLTAGE
LEVELS (AS HEREIN AMENDED)

AMENDMENT

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This amendment is in response to the Office Action
that was mailed on March 7, 2005.

IN THE TITLE:

On page 1 change the Title to read as follows:

**TITLE: SINGLE-TRANSISTOR TWO-RESISTOR CIRCUIT WHICH
 WHICH TRANSLATES TEST SIGNALS TO SELECTABLE
 VOLTAGE LEVELS**